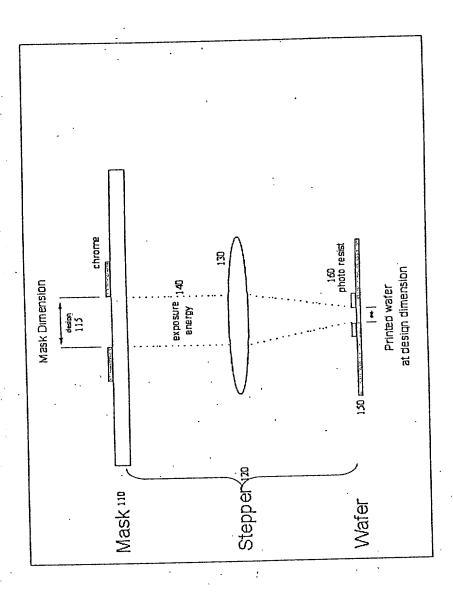
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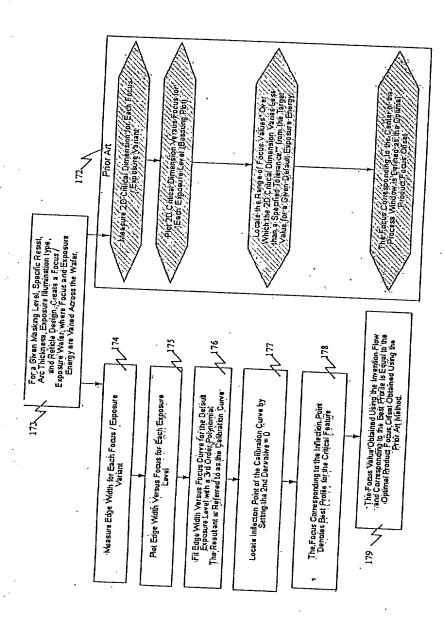


Fig. 1A

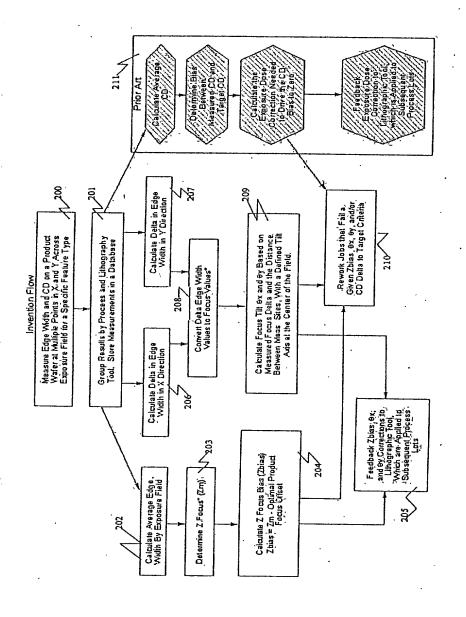
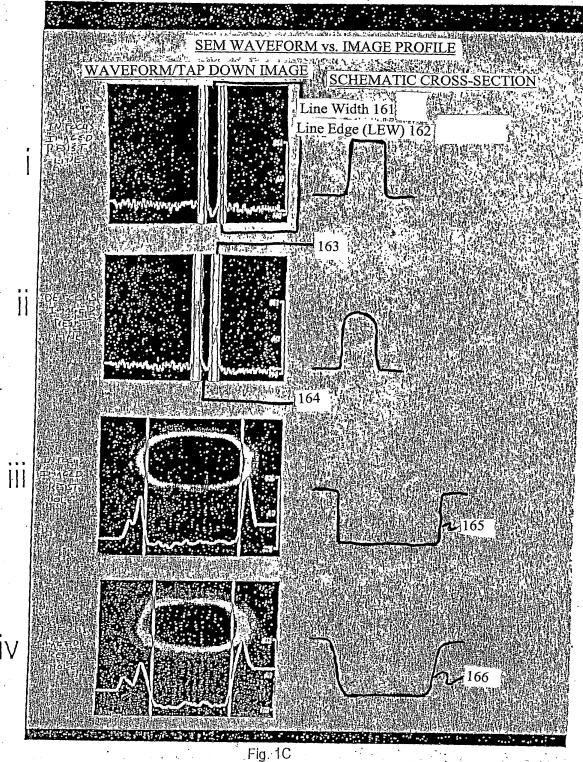


Fig. 1E



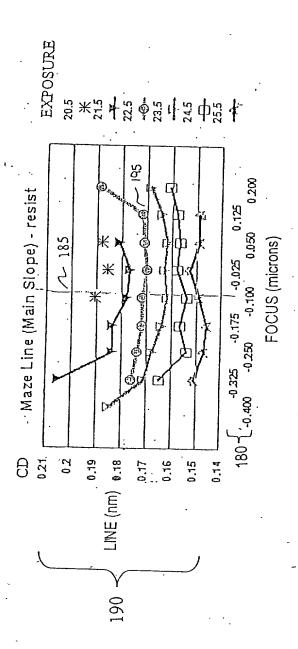
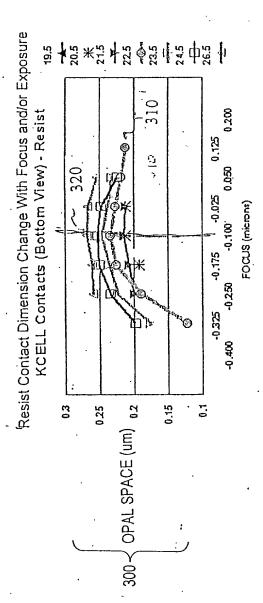


Fig. 1D



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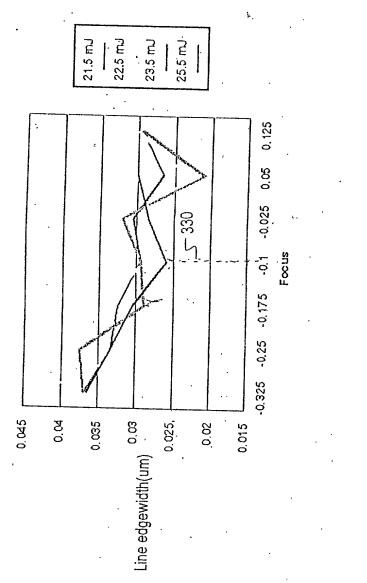


Fig 3

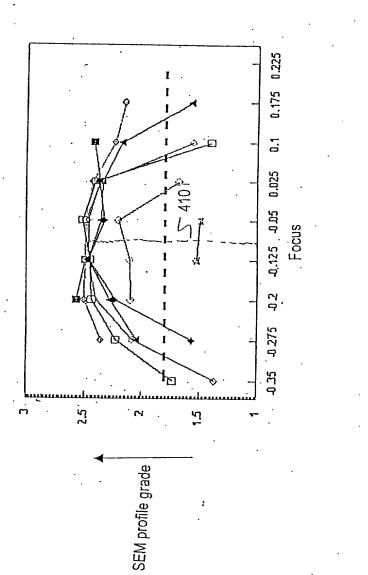


Fig. 7

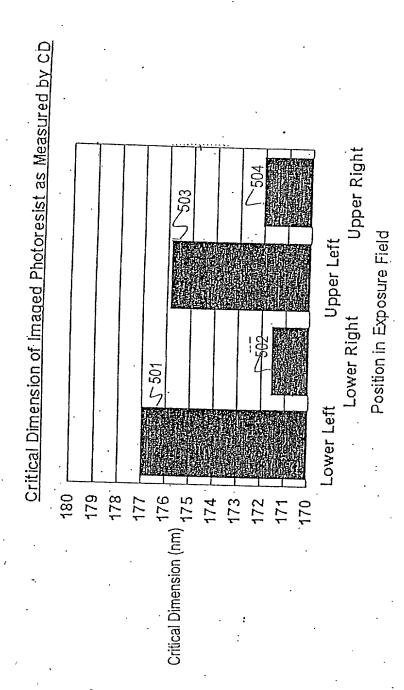


Fig. 5

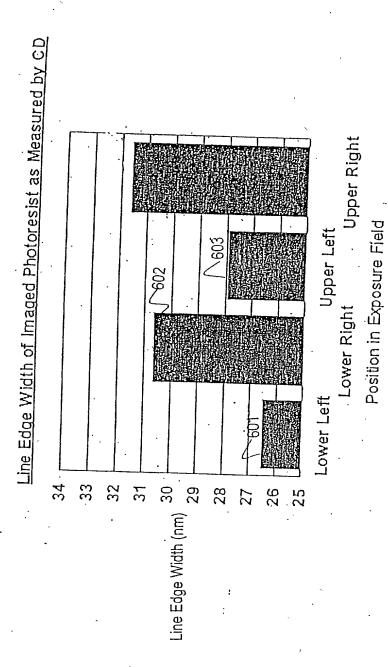


Fig. 6



Fig. 7

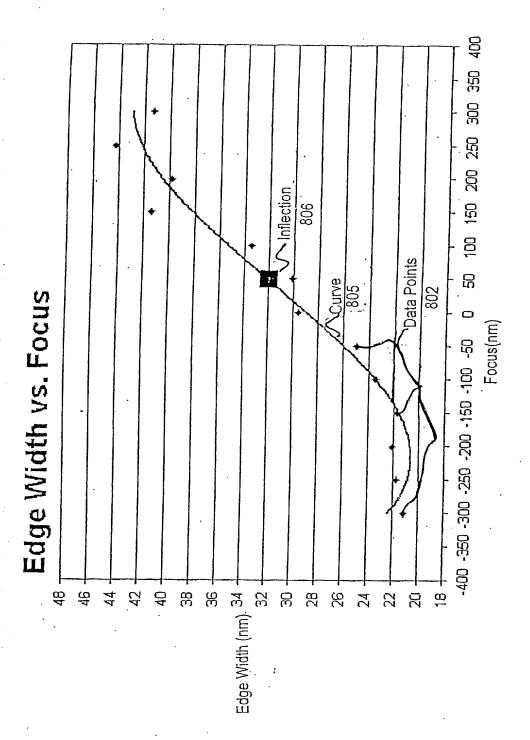
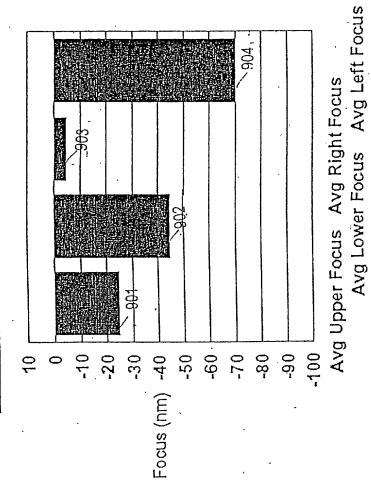


Fig. 8

X/Y Tilt Determination Using Edge Width Measurement



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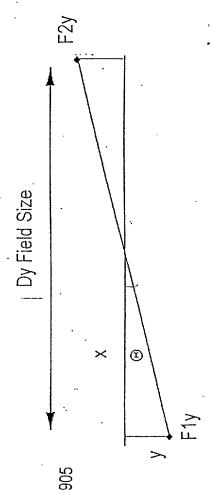
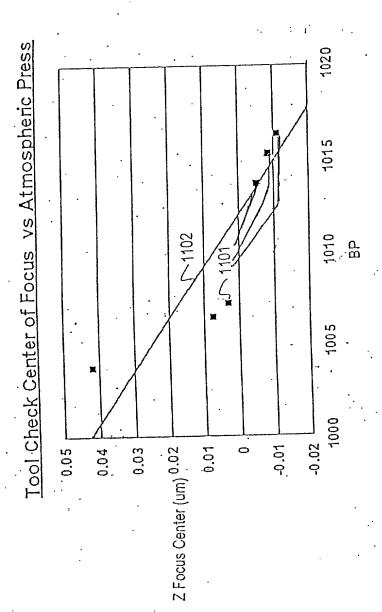


Fig. 10



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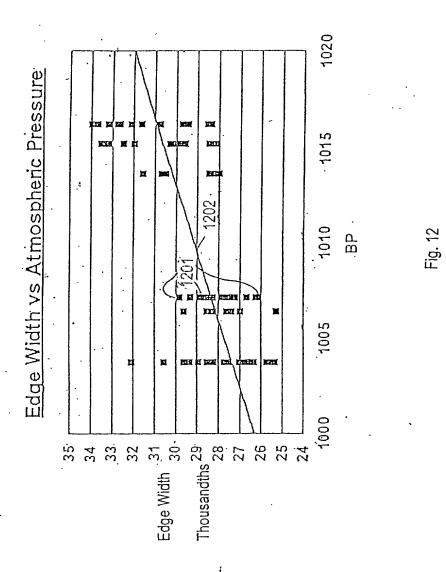


TABLE OF RUN RULES

1310 Tool	1320 Technology	1330 WaferPN	1340 Process	1350 O pt	1360 Default Exposure	1370 Focus
U82V	ICE8S3D	*	MC	F	19.00	6.18
UB2V	ICECBS2	*	МС	F	19.00	0.18
UB2V	ICEC8S3	*	МС	F	19.88	9.18
U84V	CM0S6X1	*	MC.	F	20.00	0.00
U86V	CS01950	* .	MC9S	F	24.00	-8.18
Vasu	CS019S8	9669698J6646	MC9S	F	30.00	-0.18
U86V	CS019S0	0000008J0645	MC9S	F	30.00	-0.19
V98U	CS019S0	.0000057P6438	MC9S	F	28.00	-8.85
Vasv	CS0I9S2	*	MC9S	F	25.00	-8.10
U92V -	CS019S0	*	MC9S	F	24	-0.10
U92V	CS019S8	0600068J0639	MC9S	Ę	36.66	-8.18
U92V	CS019S0	0000008J0540	MC9S	F	38.88	-0.10

Fig. 13 (Prior Art)

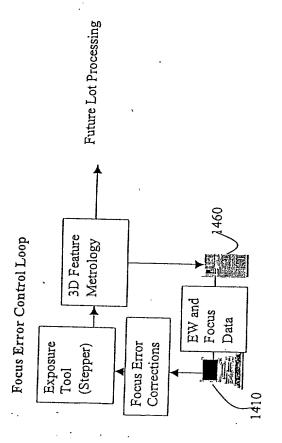


Fig. 1